Tanner EDA

Speeding Concept to Silicon

High Performance EDA Tools for Analog/Mixed Signal ICs, ASICs and MEMS

esearch, Zürich
y 09, 2012

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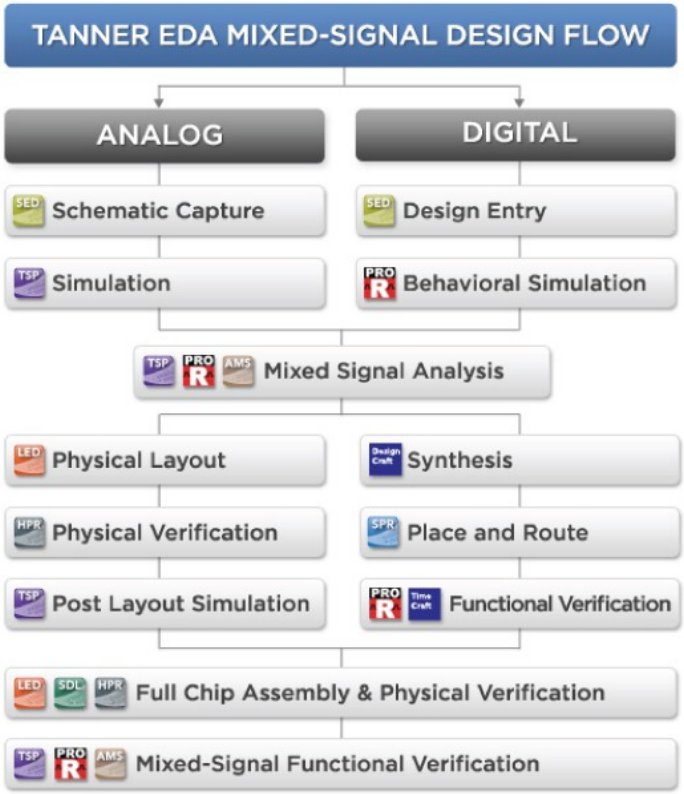
Driving Innovation: Analog & Mixed-Signal

berkeley design™

- *Optional add-in to bolster T-Spice*
- *Industry-leading performance*
- *Transient Noise Analysis*
- *S-Parameter support*

OpenAccess

- *Industry standard database format*
- *Multi-user functionality*



ALDEC Tanner EDA incentia

foundry-certified PDKs

TOWERJAZZ Dongbu HiTek XFAB

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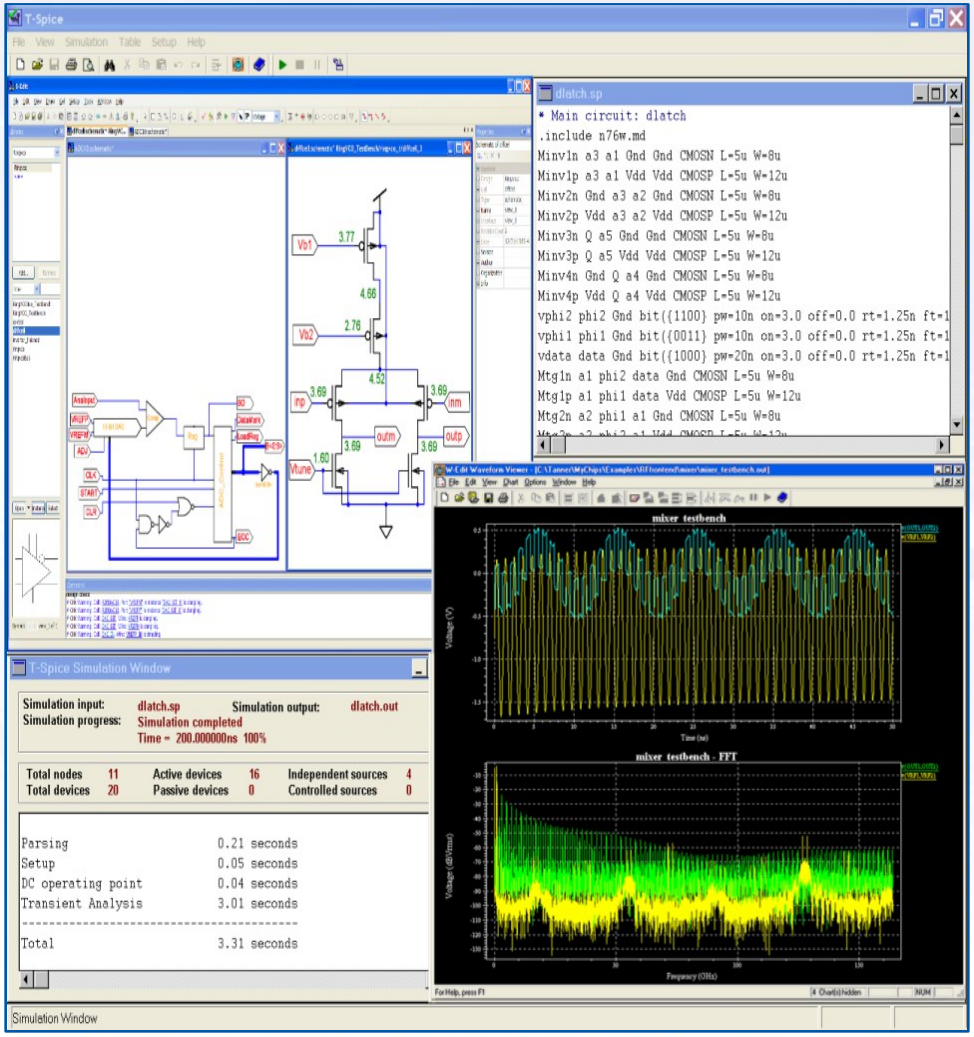
T-Spice – Analog Simulation

- Runs on Windows and Linux
- HSPICE® and PSPICE® Compatible
- Verilog-A Behavioral Modeling
- Advanced Numerical Techniques for Superior Convergence



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T-Spice – Build in Models (extract)

- PSP
- BSIM3 v3.3
- BSIM4 v4.7
- EKV v2.6,
- HiSIM260
- HiSIM_HV2
- VBIC
- BSIM SOI v4.4
- MOS 9, 11, 20
- MOS 30, 31, 40
- Philips Mextram
- Philips Modella
- Poly-Si TFT
- RPI a-Si

In total there are more than 80 compact models
Verilog A support for all new developments

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Tanner Tools for Universities and Students

- Almost free licenses through Europractice
- Easy to install, just klick on „setup.exe“
- Ideal for class rooms because of short learning curve

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For more information goto:

www.tannereda.com

Or ask me in the coffee break.

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