



Key Lab. of RF Circuits and Systems, Ministry of Education

Hangzhou Dianzi University (HDU)

Compact Modeling for Transistors Accommodating Inner Thermal Feedback Behavior

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Context



Background

Theory and Derivation

Model Development

➤ FinFET Device Modeling

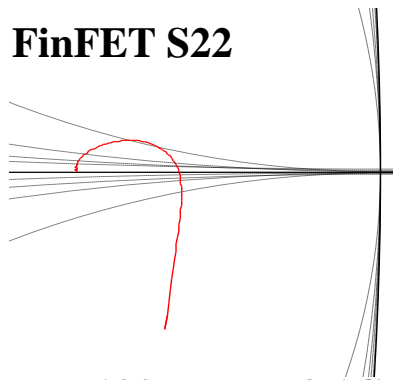
Extended Application

Summary

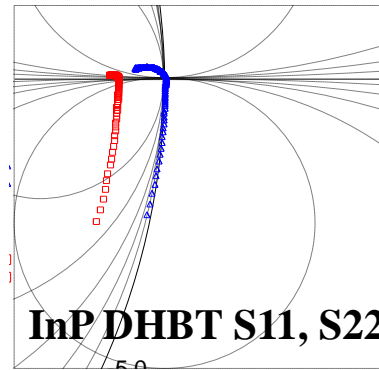
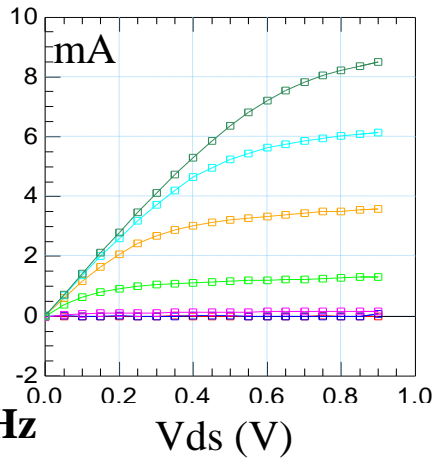
Self-heating Effect in Transistors

- ❑ DC Behaviors: device temperature rise, reduction in on-current....
- ❑ Frequency Behaviors: long signal delays, oscillation, inductive in-/output-impedance....

FinFET S22

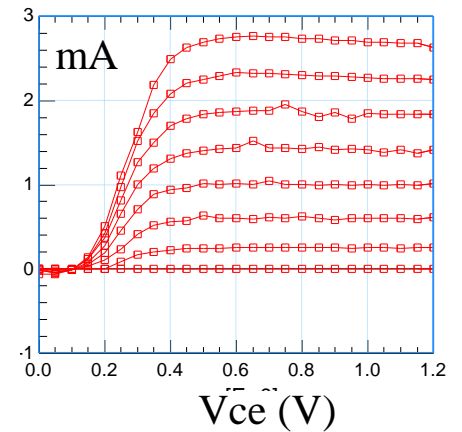


FinFET DC IV

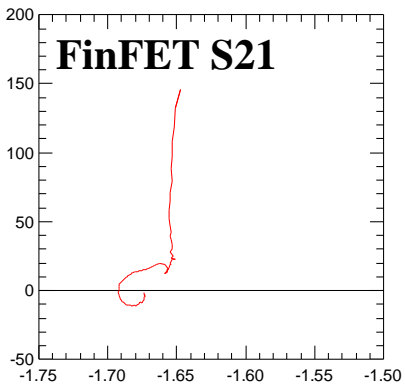


InP DHBT S11, S22

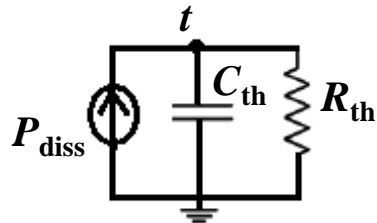
InP DHBT DC IV



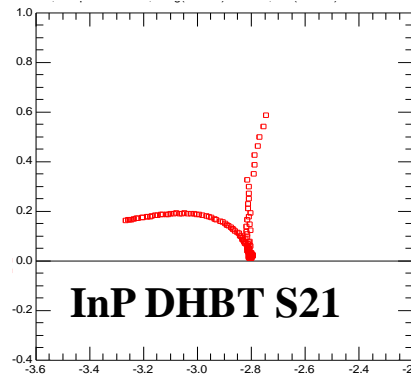
Freq: 100KHz to 3.1GHz



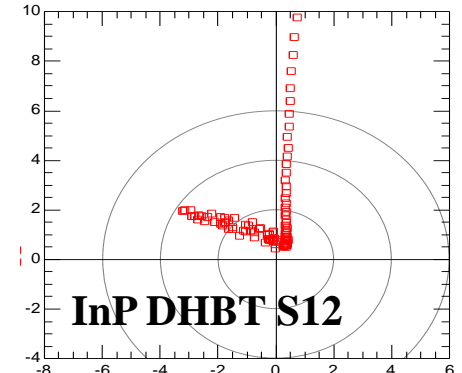
FinFET S21



Commonly Used Thermal Sub-circuit



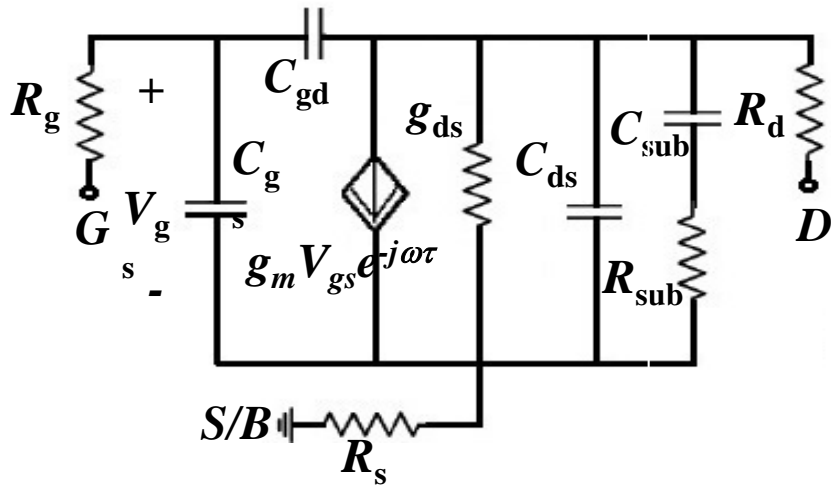
InP DHBT S21



InP DHBT S12

Commonly Used Small-signal Model for Transistors

- Model normally extracted at hi-frequency, e.g. No thermal effect considered;
- For FinFETs application, the frequency range of thermal effect can high to $\sim 1\text{GHz}$, strong thermal effect can not be omitted in device modeling.



Small-signal model for Si-FET devices.

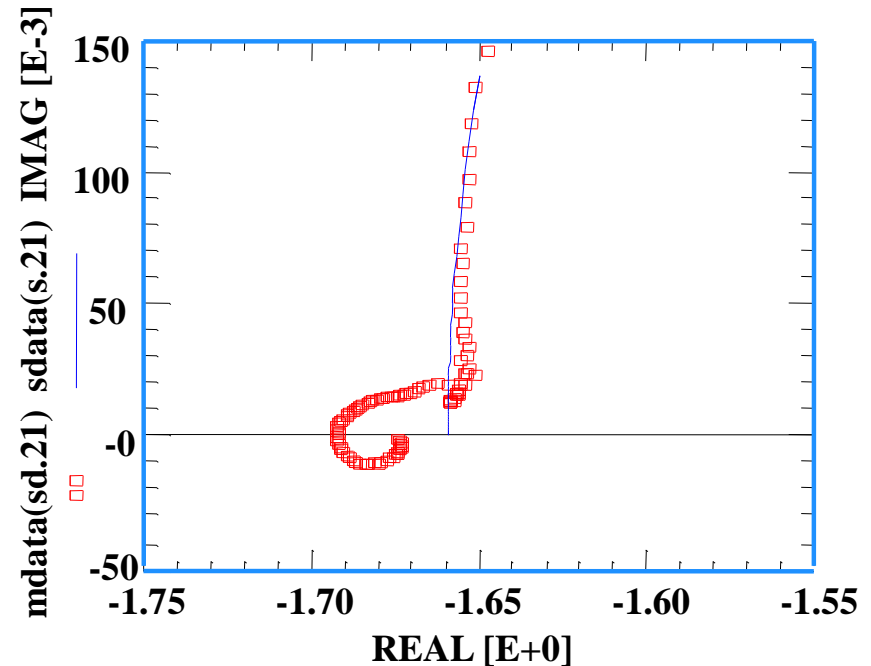


Fig. 1 Comparison of the model simulated (solid lines) and measured (symbols) Y -parameters (Y_{21}) of a FinFET device at $I_{be} = 2 \times 10^{-4}\text{A}$, $V_{ce} = 1.2\text{V}$.

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□ Summary

Derivation of the Network Parameters with Thermal Effect

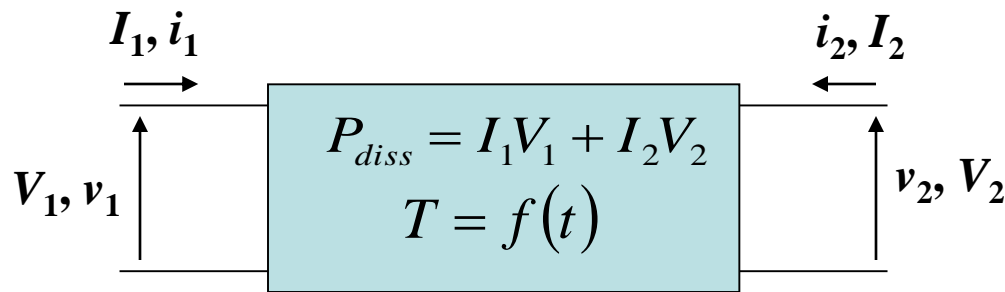


Fig.2 Voltages and currents in a two-port network.

$$I_1 = f_1(V_1, V_2, T)$$

$$I_2 = f_2(V_1, V_2, T)$$

$$T = T_{nom} + T_{rise} + V_t$$

$$V_t = Z_{th} P_{diss}$$

$$y_{mn} = \frac{dI_m}{dV_n} = \underbrace{\frac{\partial f_m}{\partial V_n}}_{\text{P1}} + \underbrace{\frac{\partial f_m}{\partial T}}_{\text{P2}} \underbrace{\frac{dT}{dV_n}}_{\text{P3}}$$

$$y_{mn}^* = \frac{\partial f_m}{\partial V_n} \quad \frac{\partial f_m}{\partial T} = M_m I_m$$

M_m : Relative changes of the I_m per degree temperature change at constant voltages.

P1: Thermal Isolated Part, $T = T_{nom}$

P2: Instance Temperature Dependent Part, $T = T_{nom} + T_{rise}$

P3: Self-heating Dependent Part, $T = T_{nom} + Z_{th} * P_{diss}$

Y-parameters Accommodating Self-heating Behavior

$$y_{mn} = \frac{dI_m}{dV_n} = \frac{\partial f_m}{\partial V_n} + \frac{\partial f_m}{\partial T} \frac{dT}{dV_n}$$

$\frac{\partial f_m}{\partial V_n}$
 $\frac{\partial f_m}{\partial T} \frac{dT}{dV_n}$

y_{mn}^*
 $M_m I_m$
P3

For P3:

$$dT = Z_{th} dP_{diss}$$

$$P_{diss} = I_1 V_1 + I_2 V_2$$

$$\frac{dT}{dV_n} = Z_{th} \frac{dP_{diss}}{dV_n}$$

$$dP_{diss} = I_1 dV_1 + V_1 dI_1 + I_2 dV_2 + V_2 dI_2 \quad , \text{ for } y_{m1}, dV_2 = 0; \text{ for } y_{m2}, dV_1 = 0$$

$$\Rightarrow y_{mn} = \frac{y_{mn}^* + M_m Z_{th} I_m I_n}{1 - M_m Z_{th} P_{diss}} \quad (\text{Ref. O. Mueller, 1964})$$

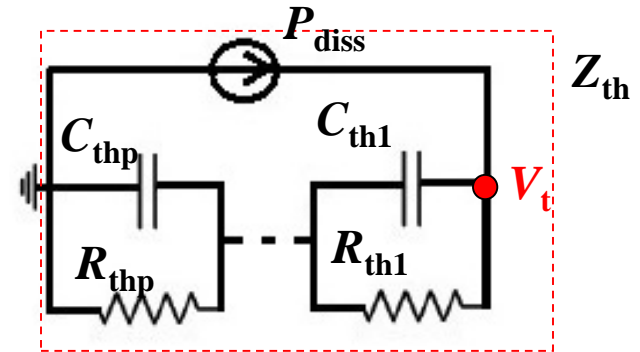


Fig.3 General thermal equivalent circuit

$$Z_{th} = \sum_{k=1}^p Z_{thk} = \sum_{k=1}^p R_{thk} / (1 + j\omega R_{thk} C_{thk})$$

Compact Model Development

$$y_{mn} = \frac{y_{mn}^* + M_m Z_{th} I_m I_n}{1 - M_m Z_{th} P_{diss}}$$

Only DC consideration: $D_{ij} = -M_i (P_{diss} I_i / V_i + I_i I_j)$

$$y_{mn}^* = y_{mn} - Z_{th} M_m (y_{mn} P_{diss} + I_m I_n) = y_{mn} + Z_{th} D_{mn}$$

$$Z_{th} = \sum_{k=1}^p Z_{thk} = \sum_{k=1}^p (R_{thk}^{-1} + j\omega R_{thk} C_{thk})^{-1}$$

$$Z_{th} D_{mn} = D_{mn} \sum_{k=1}^p Z_{thk} = \sum_{k=1}^p \left[(D_{mn} R_{thk})^{-1} + j\omega C_{thk} D_{mn}^{-1} \right]$$

\downarrow R_{mnk}^* \downarrow L_{mnk}^*

$$\Rightarrow y_{mn}^* = y_{mn} + \sum_{k=1}^p (R_{mnk}^* + j\omega L_{mnk}^*)^{-1}$$

Enables complete small-signal model for transistors.

Small-signal Model Topology Development

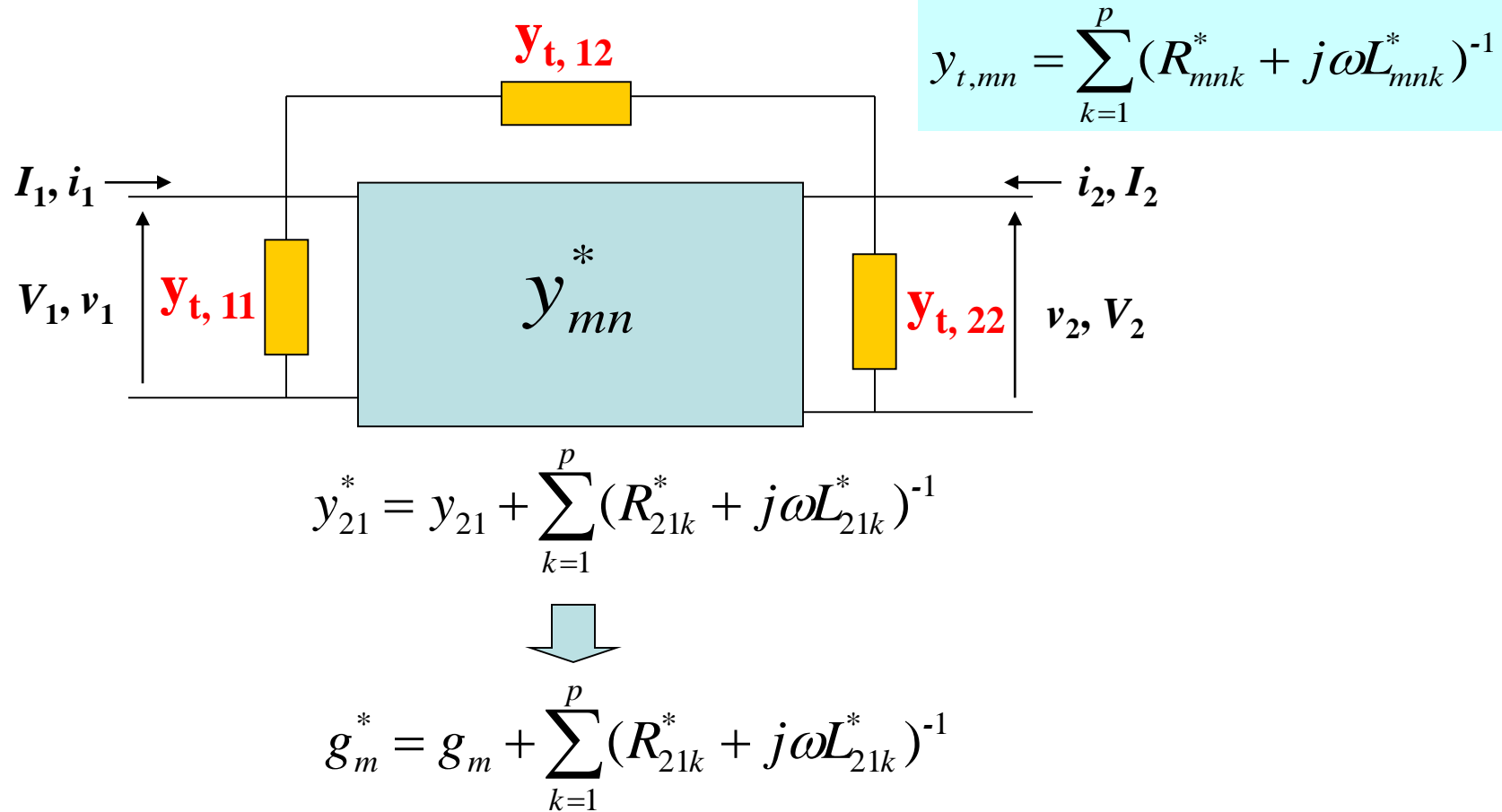


Fig.4 Complete small-signal model for transistors with thermal feedback caused by self-heating effect.

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➤ **FinFET Device Modeling**

□ Extended Application

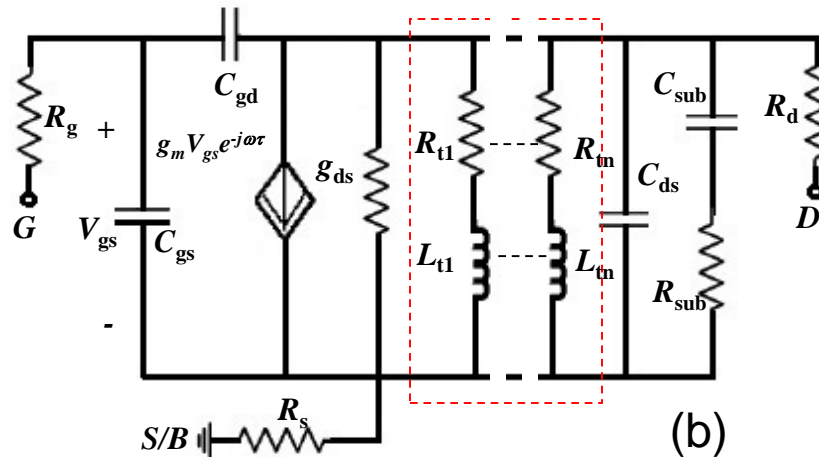
□ Summary

FinFET Devices Modeling

□ For FinFET devices, $I_1 = I_{\text{gate}} < 1.0\text{e-}9$ A, self-heating is mainly caused by $I_2 = I_{\text{ds}}$

dP_{diss} can be simplified to $dP_{\text{diss}} = I_2 dV_2 + V_2 dI_2$

y_{t_11} and y_{t_12} are then can be omitted.

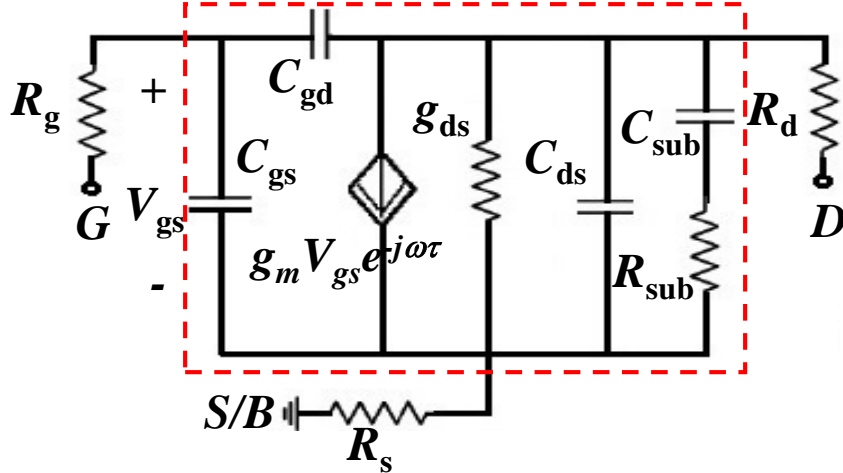


$$g_m = g_m^* \left[1 + \sum_{i=1}^n \frac{k_{shi}}{1 + j\omega\tau_{shi}} \right] \quad k_{shi} = -M_2 P_{\text{diss}} R_{thi}, \quad \tau_{shi} = R_{thi} C_{thi}$$

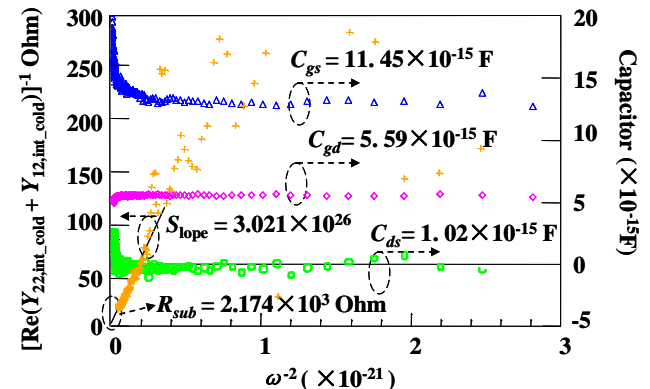
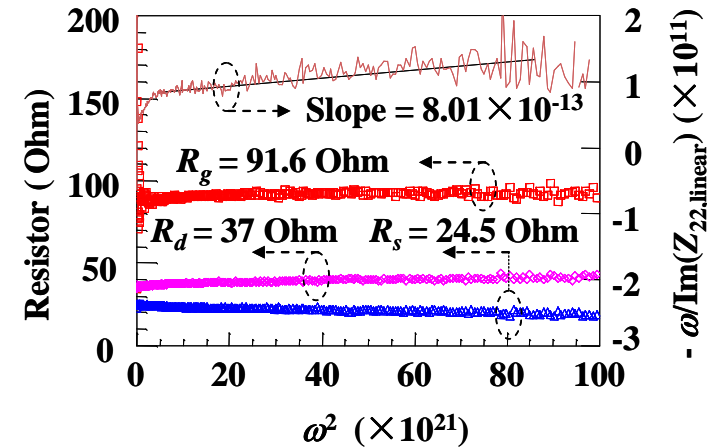
Figure 5. Proposed small-signal model for FinFETs

Model Parameter Extraction – HF Elements

- Self-heating effect is a low frequency behavior, HF elements can be extracted from $Y|_{\text{HF}}$ at first, the thermal feedback elements are then can be extracted from low frequency Y -parameters, $Y|_{\text{LF}}$, analytically.



- R_s , R_{sub} and R_g are extracted by using the methods proposed in [10]. Bias condition $V_{\text{ds}} = 0$ V and $V_{\text{gs}} = 0.9$ V is used.
- Once the terminal elements are extracted, the inner y-parameters are used to extract inner elements.



Thermal Feedback Elements Extraction

$$\square dY = Y|_{\text{HF}}(2, 2) - Y|_{\text{LF}}(2, 2) = \sum_{k=1}^p (R_{22k}^* + j\omega L_{22k}^*)^{-1}$$

$$\square dY = Y|_{\text{HF}}(2, 1) - Y|_{\text{LF}}(2, 1) = \sum_{k=1}^p (R_{21k}^* + j\omega L_{21k}^*)^{-1}$$

TABLE I

Initially Extracted and Optimized Values of the Model Parameters at Bias 1: $V_{gs} = 0.6$ V, $V_{ds} = 0.9$ V. RMS Error = $|\text{Extracted Value} - \text{Optimized Value}| / \text{Extracted Value} \times 100\%$.

Model Parameters for SHE Characterization Only are Marked with Symbol *.

Parameter	Extracted Value	Optimized Value	Error (%)
$R_{g/d/s}$ (Ω)	91.6, 37, 24.5	94.9, 39.8, 21.4	3.6, 7.5, 12.6
R_{sub} (k Ω), τ (pS)	2.295, 1.06	2.21, 0.92	4.1, 13.2
$C_{gs/d}$ (fF)	11.45, 5.59, 11.46, 5.39, 0.8, 3.5		
C_{ds} , C_{sub} (fF)	1.02, 1.23	0.91, 1.34	10.7, 8.9
* $R_{t1/2/3}$ (k Ω)	204, 7.5, 8.4	198, 7.4, 8.6	2.9, 1.3, 2.3
* $L_{t1/2/3}$ (μ H)	59k, 90, 24.6	57k, 87.6, 23.2	3.4, 2.7, 5.7
g_{ds} , g_m (mS)	0.72, 28.1	0.71, 25.82	1.3, 8.1
* $\tau_{sh1/2/3}$ (nS)	290, 12.1, 2.9	266, 12.9, 2.7	8.2, 6.6, 6.9
* $k_{sh1/2/3}$ ($\times 10^{-3}$)	-13, 28.3, 21.6	-14.2, 26.5, 22.1	9.2, 6.3, 2.3

Model Verification

- Bulk FinFET, 4-Fin N-MOSFET with a total number of fingers $N_f = 32$ and gate length $L = 16$ nm, is fabricated using SMICs 14 nm bulk standard technology .
- A roughly frequency, $f_{sh} = 0.75$ GHz can be observed in the image parts of Y_{21} and Y_{22} where self-heating effect is negligible when $\text{freq} > f_{sh}$.

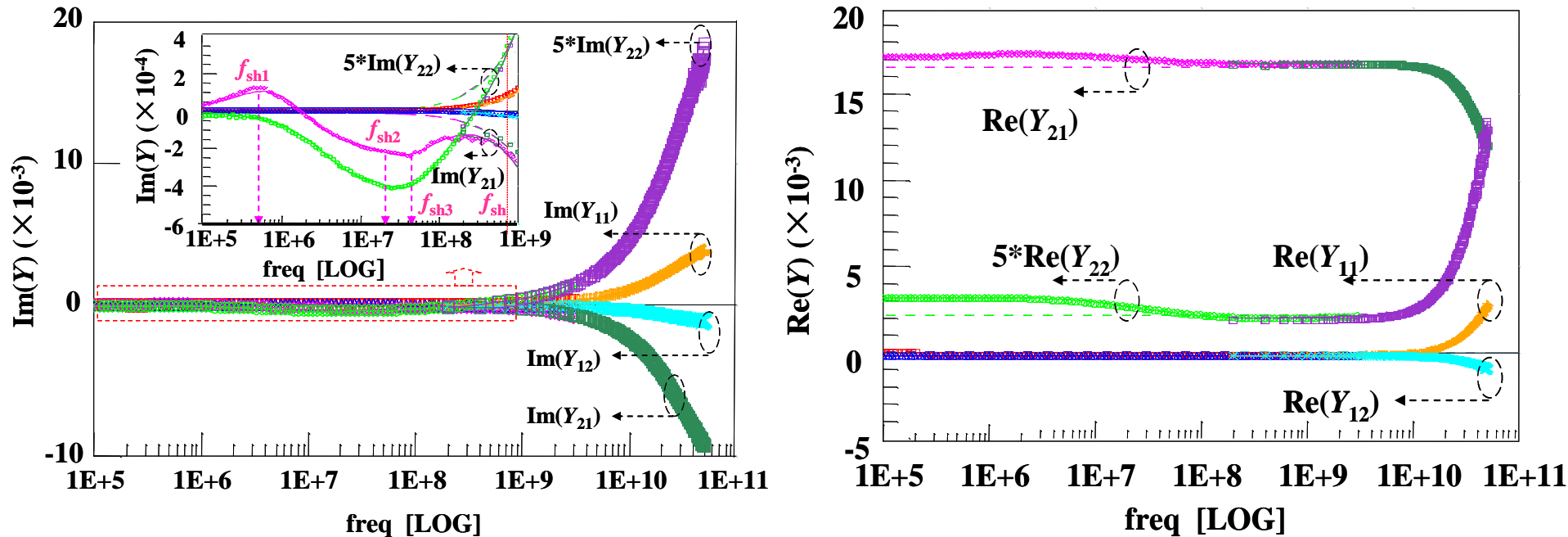



Fig. 6. Comparison of the model simulated with (solid lines) / without (dashed lines) SHE and measured (symbols) Y -parameters at $V_{gs} = 0.6$ V, $V_{ds} = 0.9$ V.

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 - **FinFET Device Modeling**
- **Extended Application**
-  □ **Summary**

Summary and Discussion

- A novel small-signal model for transistors accommodating self-heating behavior is presented.
- Based on the model theory, a method to accurately extract the thermal model of transistors is proposed.
- Discussion: **Is the thermal model can be extracted from Pulse IV?**
 - Thermal isolated testing: the time width of the pulse must less than $1/(2*\pi*f_{sh})$;
 - R_{thi} and C_{thi} can not be extracted from Pulse IV.

TABLE I

Extracted FinFET thermal Model Parameters at Bias: $V_{gs} = 0.6$ V, $V_{ds} = 0.9$ V.

Parameter	Extracted Value	Optimized Value	Error (%)
$\tau_{sh1/2/3}$ (nS)	290, 12.1, 2.9	266, 12.9, 2.7	8.2, 6.6, 6.9



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Thanks !